

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.:

Group Art Unit:

Inventor: Dudovicz

Filed: Concurrently

Title: POLISHING SILICON WAFERS

Examiner:

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 CFR 1.51, 1.56 and 1.97 to 1.99, the list of references cited on the attached Form PTO-1449 is made of record to assist the Patent and Trademark Office in its examination of this application. A copy of each of the references cited is enclosed herewith. Since we have not received an Office Action on the merits, no fee is believed to be due.

This disclosure statement should neither be construed as a representation that a search has been made, nor as an admission that the information cited is, or is considered to be, material to patentability as defined in 1.56(b).

Respectfully submitted,



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Date: October 21, 2003

Form PTO-1449 (Rev. 8-83)		U.S. Department of Commerce		Atty. Docket No. CS-21,274-3		Serial No.							
Information Disclosure Citation (Use several sheets if necessary)				Applicant Dudovicz									
				Filing Date		Group							
U.S. PATENT DOCUMENTS													
Examiner Initial		Document Number						Date	Name	Class	Subclass	Filing Date if Appropriate	
				2	7	8	1	7	4/1860	Mayall	51	297	
				8	1	9	8	6	9/1868	Crane	451	533	
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		Fynn et al., "The Cutting and Polishing of Electro-optic Materials" (John Wiley and Sons, 1979) pp 82-83											
Examiner										Date Considered			
* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.													

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FOREIGN PATENT DOCUMENTS													
		Document Number						Date	Country	Class	Subclass	Translation	
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